

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.

212005US2

SERIAL NO.

09/919,859

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Hans Jurgen MATTAUSCH, et al.

FILING DATE

August 2, 2001

GROUP

2188

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

RECEIVED

JUN 14 2004

Technology Center 2100

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
PMV	AO	54-066277	05/29/1979	JAPAN (w/English abstract)		X
PMV	AP	08-036525	02/06/1996	JAPAN (w/English abstract)		X
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

PMV	AW	SHIMADA Sou, ANDO Hideki and SHIMADA Toshio, "Multi-bank Cache having no Cross-Bus Switch", reports of 2000 Memorial Parallel Processing Symposium (JSPP2000), Information Processing Society of Japan, 30 May 2000, pp. 107-114.				
PMV	AX	SASAKI Takayasu, TSUCHIE Tatsuo, HIRONAKA Tetsuo and KOJIMA Akira, "Study of Method of Implementing Data Cache for Superscalar Processor", Report of the Information Processing Society of Japan, 29 August 1996, Vol. 96, No. 80 (96-ARC-119), pp. 215-220.				
PMV	AY	NATOMI Akira, KUGA Morihiro, MURAKAMI Kazuaki and TOMITA Masaharu, "Load/Store Pipeline of DSN-mode Superscalar Processor Prototype", 24 January 1991, Vol. 91, No. 9, (91-ARC-86), pp. 1-8.				
	AZ					

☐ Additional References sheet(s) attached

Examiner

Lynn H. Jale

Date Considered

7/28/04

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.